Amendments to the claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of claims:

Claim 1 (currently amended): Method for controlling ~~the~~ a phase of successively transmitted frames, in which data symbols are transmitted at a constant symbol frequency, ~~in which method~~comprising the steps of:

determining a phase difference between ~~the~~ a clock of ~~the~~ a frame transmission and a data clock corresponding to the frequency at which the data symbols are transmitted;

~~is determined and dependent on the phase difference~~producing an adjusting signal ~~is produced for controlling the~~ based on the determined phase difference in order to control an injection of stuffing data symbols into the frames for changing ~~the~~ frame length and the phase of the frame transmission, whereby the phase of the frame transmission is controlled so that the frames are transmitted on average synchronously with the data clock,

wherein the adjusting signal is produced dependent on the phase difference determined from N frames in each case successively transmitted, whereby N is at least equal to 2.

Claim 2 (original): Method according to claim 1, wherein the controlling, according to which the adjusting signal is produced, has a proportional section and an integral section.

Claim 3 (original): Method according to claim 1, wherein the controlling of the adjusting signal has a large limit cycle.

Claim 4 (original): Method according to claim 1, wherein the controlling of the adjusting signal is set up in such a manner that it has a large limit cycle and a small limit cycle, the adjusting signal movement of which has a higher frequency than the adjusting signal movement of the large limit cycle and during which stuffing data symbols are injected in two alternating amounts into the frames.

Claim 5 (original): Method according to claim 1, wherein the adjusting signal can assume several different conditions, whereby the different conditions designate different amounts of injected stuffing data symbols.

Claim 6 (original): Method according to claim 1, wherein the adjusting signal is produced dependent on the phase difference averaged in each case over N frames successively transmitted.

Claim 7 (original): Method according to claim 1, wherein the controlling of the adjusting signal is time- and amplitude-discrete.

Claim 8 (original): Method according to claim 1, wherein for determining the phase of the transmitted frames and the data clock or for determining the phase difference thereof it is recorded in which periods of a signal with a phase measurement frequency an edge of the data clock or the beginning of a new frame arises.

Claim 9 (original): Method according to claim 8, wherein the phase measurement frequency is an integral multiple of the symbol frequency.

Claim 10 (original): Method according to claim 1, wherein N is equal to 2.

Claim 11 (currently amended): Phase detector for use in a device for controlling ~~the~~ a phase of successively transmitted frames, in which data symbols are transmitted at a constant symbol frequency, ~~whereby~~ comprising:

the phase detector ~~is set up in such a manner that it determines~~is configured to determine a phase difference between ~~the~~ a clock of ~~the~~ a frame transmission and a data clock corresponding to the frequency at which the data symbols are transmitted, and ~~produces~~ produce an output signal, ~~whereby the phase detector is set up in such a way that it produces~~ the output signal being dependent on the phase difference determined from N frames in each case successively transmitted, whereby N is at least equal to 2, and ~~whereby the~~

a controlling device ~~is set up in such a manner that dependent on the output signal of the phase detector~~configured to produce an adjusting signal ~~is produced~~based on the output signal of the phase detector ~~for controlling the~~in order to control an injection of stuffing data symbols into the frames for changing ~~the~~ frame length and the phase of the frame transmission and controls the phase of the frame transmission so that the frames on average are transmitted synchronously with the data clock.

Claim 12 (currently amended): Device for controlling ~~the~~ a phase of successively transmitted frames in which data symbols are transmitted at a constant symbol frequency, comprising:

~~with~~ a phase detector for determining a phase difference between ~~the~~ a clock of ~~the~~ a frame transmission and a data clock corresponding to the frequency at which the data symbols are transmitted; and

an adjusting signal generating device, which is connected to the phase detector, and ~~set up in such a way that dependent on the phase difference it produces~~configured to produce an adjusting signal based on the determined phase difference in order to control ~~for controlling the~~an injection of stuffing data symbols into the frames for changing the frame length and the phase of the frame transmission,

whereby the device is ~~set up in such a manner that it controls~~configured to control the phase of the frame transmission so that the frames on average are transmitted synchronously with the data clock, and whereby the device is set up in such a manner that it produces the adjusting signal dependent on the phase difference determined from N frames in each case successively transmitted, whereby N is at least equal to 2.

Claim 13 (original): Device according to claim 12, wherein the adjusting signal generating device has a controller block with a proportional section and an integral section.

Claim 14 (original): Device according to claim 12, wherein the adjusting signal generating device has a threshold switch, which is set up in such a manner that it supervises an input signal received by it in relation to over or under-stepping at least one limit value and dependent on this produces the adjusting signal.

Claim 15 (original): Device according to claim 12, wherein the device is integrated in a semiconductor module.

Claim 16 (original): Device according to claim 12, wherein the signals are processed digitally in the device.

Claim 17 (original): Device according to claim 12, wherein the signals are processed in the device by execution of a program in a microprocessor.